

ABSTRACT OF THE DISCLOSURES

A low-capacitance laminate varistor has at least one pair of first and second inner electrodes; a varistor layer, formed by at least one pair of first and second outer electrodes and the varistor layer being laminated; and the first outer electrode and the second outer electrode are electrically connected to the first inner electrode and a second inner electrode, respectively. In the low-capacitance laminate varistor of the present invention, the first inner electrode and the second inner electrode are staggered and also formed on the same plane and parallel to one another so that the electrode surface of the first inner electrode does not face the electrode surface of the second inner electrode.